

Semiconductor Energy Laboratory Internship Graduate Projects

Term	Stay Period in SEL	University	Major	Theme/Master's thesis title
Term 1	2009.8~2010.2	Uppsala University	Technology and Science	Taguchi DoE for microcrystalline silicon i-layers in solar cells
	2009.9~2010.3	Uppsala University	Engineering	Characterization of Boron Doped Layers in Thin Film Silicon Solar Cells – Material Properties and Performance
	2009.9~2010.3	KTH Royal Institute of Technology	Computer Engineering	Implementing an Actor-Model-Like Concurrent RFID System for T-Engine, Combining Frequency and Time Domain Multiple Access
Term 2	2011.1~2011.10 ※Including the period of temporary return due to the earthquake	Uppsala University	Technology and Science	“Characterization of Intrinsic Regions and Cell Efficiency in Hetero-junction Structured Silicon Solar Cells – Material Properties and Performance”
	2011.1~2011.9 ※Including the period of temporary return due to the earthquake	Uppsala University	Information Technology Engineering Program	Comparing SIFT and SURF: Performance on patent drawings
	2011.1~2011.10 ※Including the period of temporary return due to the earthquake	Karlstad University	Physical Electronics	Reduced Graphene Oxide Coating on Lithium-Ion Battery Electrode Reduced Graphene Oxide Coating
Term 3	2013.4~2013.10	Linköping University	Applied Physics and Electronic Engineering	Realizing a 32-bit Normally-Off Microprocessor With State Retention Flip Flops Using Crystalline Oxide Semiconductor Technology
Term 4	2014.2~2014.8	Chalmers University of Technology	Computer Science and Engineering	A study of ambient light-independent multi-touch acquisition and interaction methods for in-cell optical touchscreens
	2014.2~2014.8	Chalmers University of Technology		
Term 4	2014.2~2014.8	Linköping University	Applied Physics and Electronic Engineering	Demonstration of Dynamic Reconfiguration in a Crystalline IGZO-based Multi-Context FPGA
Term 5	2015.5~2015.11	KTH Royal Institute of Technology	Embedded System	Energy Efficient Motion Capturing System Using Low-Power Image Sensor
	2015.5~2015.11	KTH Royal Institute of Technology	Embedded System	Efficient Motion Capturing and Variable Refresh Rate Display System utilizing CAAC-IGZO semiconductor FETs.
Term 6	2015.9~2016.3	Uppsala University	Science and Technology	A new correlation between accommodation and natural3D
	2016.2~2016.8	Linköping University	Applied physics and electrical engineering	Normally Off Computing System Using Multi-Context FPGA and Cortex-M0 Based on CAAC-IGZO Technology
Term 7	2021.1~2021.8 ※Online	Chalmers university of Technology, Gothenburg.	Applied physics	Predicting physical properties of NMCM cathode materials using machine learning guided DFT simulations
	2021.1~2021.7 ※Online	Norwegian University of Science and Technology	Physics	Electrochemical and Photo-Physical Investigations of Organic Light Emitting Diode Materials
	2021.1~2021.8 ※Online	Norwegian University of Science and Technology	Electronic Systems	Ultra-Low Power SAR-ADC in 60-nm C-Axis Aligned Crystalline Indium-Gallium-Zinc Oxide FET Integrated With 45-nm Si CMOS